

DEPARTMENT OF ELECTRICAL COMMUNICATION ENGINEERING
Indian Institute of Science, Bangalore 560 012.

I TERM : AUGUST-DECEMBER 2011
COURSES SCHEDULE

SESSION: 2011-2012

Course No. Credits	Title of the Course	Instructor/s
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I M.E. (Telecommunication):

Core

E2 202	3:0	Random Processes	Utpal Mukherji/Anurag Kumar
E2 211	3:0	Digital Communication	B Sundar Rajan
E2 221	3:0	Communication Networks	A Chockalingam

Note: 1. Students cannot choose as Electives other courses which are equivalent to the Hardcore courses.

Soft Core A

E1 251	3:0	Linear and Nonlinear Optimization	K R Ramakrishnan, EE
E2 201	3:0	Information Theory	Rajesh Sundaresan
E9 221	3:0	Digital Signal Compression	T V Sreenivas
MA 251	3:0	Numerical Methods	Mahesh Kumari, MA

Soft Core B

E2 205	3:0	Error Control Codes	P Vijay Kumar/Navin Kashyap
E2 223	3:0	Communication Protocols	P Venkataram/Anandi G
E7 221	2:1	Fibre-Optic Communication	T. Srinivas/E. S. Shivaleela
E9 201	3:0	Digital Signal Processing	A G Ramakrishnan

I M.E.(Signal Processing) 2011-12 Registration & Administration by EE

CORE

E2 202	3:0	Random Processes	Utpal Mukherji/Anurag Kumar
E2 212	3:0	Matrix Theory	Chandra R Murthy
E9 291	2:1	DSP System Design	G N Rathna, EE

Soft Core A

E1 251	3:0	Linear and Nonlinear Optimization	K R Ramakrishnan, EE
E9 201	3:0	Digital Signal Processing	A G Ramakrishnan
E9 221	3:0	Digital Signal Compression	T V Sreenivas

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Soft Core B

E2 211	3:0	Digital Communication	B Sundar Rajan
E9 231	3:0	Digital Array Signal Processing	K V S Hari
E9 241	2:1	Digital Image Processing	K Rajgopal, EE

I.M.E. (Microelectronic Systems)**Pool A: Materials, Processes and Device Technology**

E3 225	3:0	Submicron device Modeling and Simulation	S Mahapatra, CEDT
E3 327	2:1	Nanoelectronics Device Fabrication and Characterization	Navakanta Bhat/ S A Shivashankar/K N Bhat
E3 262	2:1	Electronics systems Packaging	GAR/NSD/NVCR, CEDT
E7 213	3:0	Introduction to Photonics	Manoj Varma/Ambarish Ghosh

Pool B: Circuits, CAD, Systems and Applications

E0 283	3:0	CAD Algorithms for VLSI Physical Design	To check
E0 284	2:1	Digital VLSI Circuits	Bharadwaj Amrutur
E3 231	2:1	Digital Systems Design with FPGAs	Kuruvilla Varghese, CEDT
E3 235	2:1	Analog and Data Conversion Systems	K Gunasekaran, CEDT
E3 237	3:0	Integrated Circuits for Wireless Communication	Gaurab Banerjee

ELECTIVE

E0 266	3:0	Topics in Ubiquitous Computing	P Venkataram
E2 301	3:0	Topics in Multiuser Communication	Vinod Sharma
E2 302	3:0	Next Generation Wireless Systems: Design and Analysis	Neelesh B Mehta
E2 331	3:0	Advanced Course in Coding Theory	B Sundar Rajan
E7 211	3:0	Photonics Integrated Circuits	T Srinivas/T Badrinarayana
E8 311	2:1	Advanced Topics in Electromagnetics	K J Vinoy

II M.E

EP 299	0:9	Project	Faculty
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Note 2: Students are advised to get the Registration Cards duly signed by the Advisor and respective course instructors on 01/08/2011 and keep them ready for Registration. The Course Registration will be on 02/08/2011 **from 10.30 AM to 12.30 AM and 2.30 PM to 4.30 PM in Room No.1.06 of the ECE Dept.**

Date:

(CHAIRMAN)